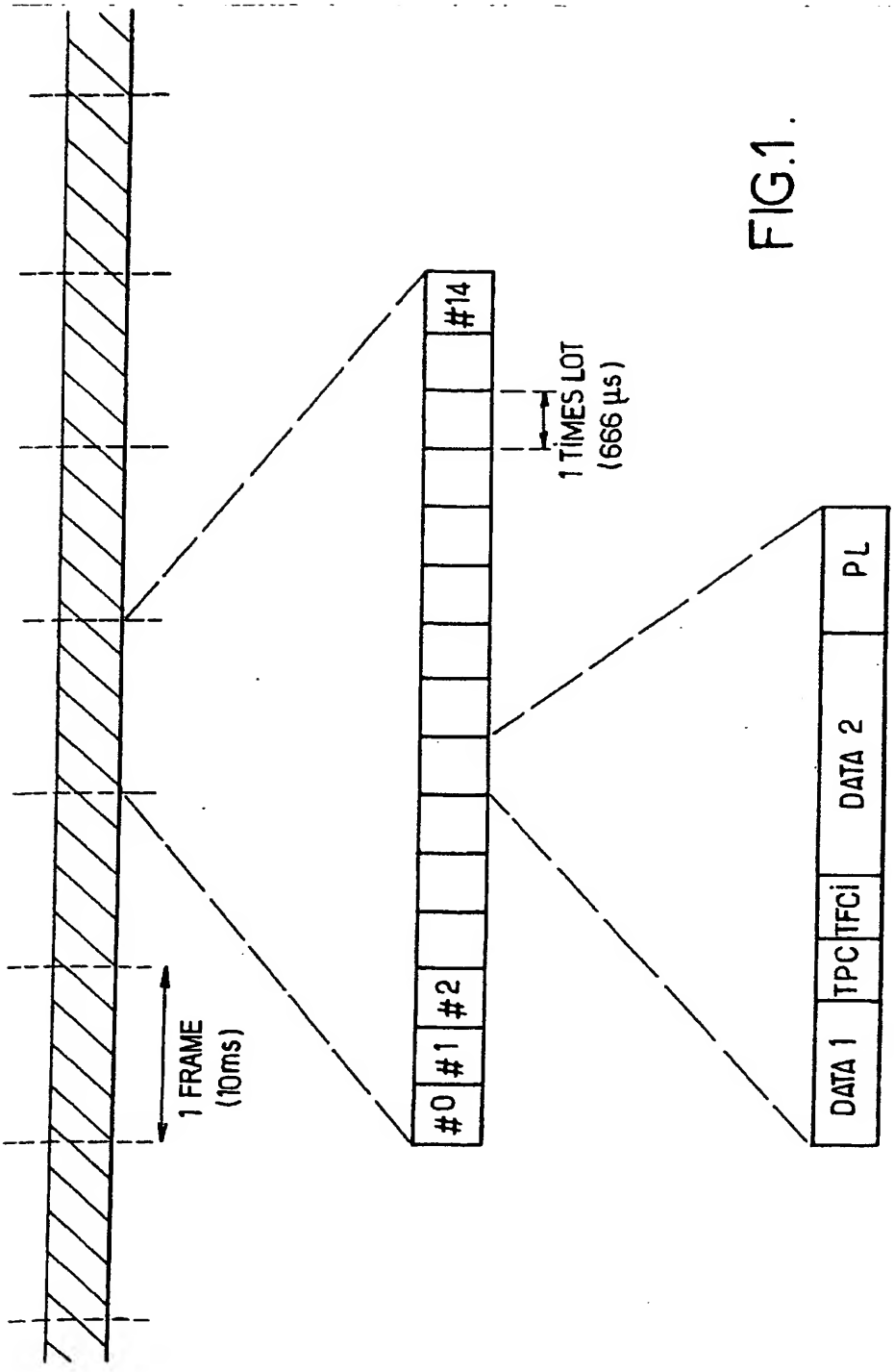


FIG.1.

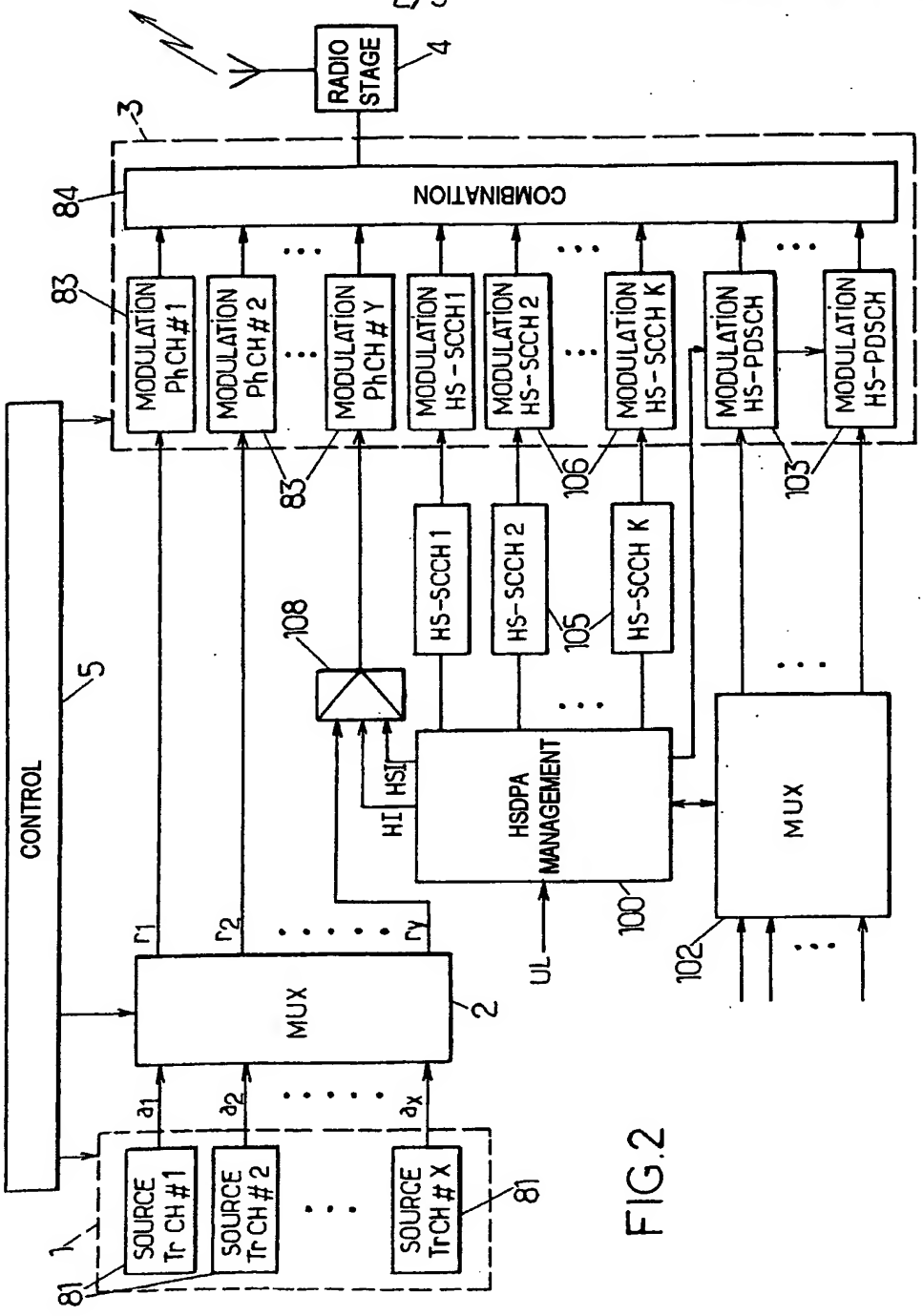
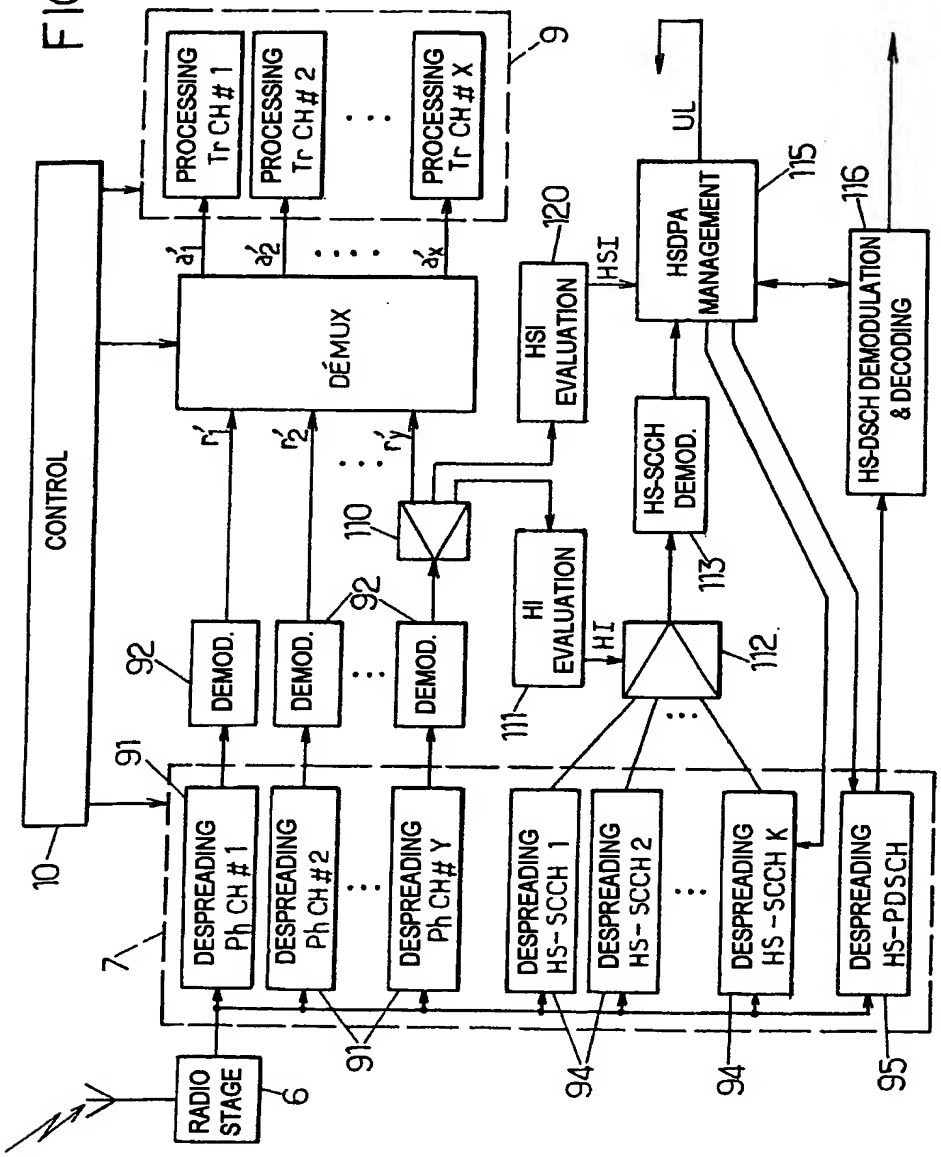

RADIO STAGE
4
3
COMBINATION
84
MODULATION PhCH#1
MODULATION PhCH#2
MODULATION PhCH#Y
MODULATION HS-SCCH1
MODULATION HS-SCCH2
MODULATION HS-SCCH K
MODULATION HS-PDSCH
MODULATION HS-PDSCH
83
106
103
HS-SCCH 1
HS-SCCH 2
HS-SCCH K
105
108
CONTROL
5
HI
HSI
HSDPA MANAGEMENT
100
MUX
102
UL
r1
r2
rγ
MUX
2
a1
a2
ax
SOURCE TrCH#1
SOURCE TrCH#2
SOURCE TrCH#X
81
1

FIG.2

10/500900

FIG.3



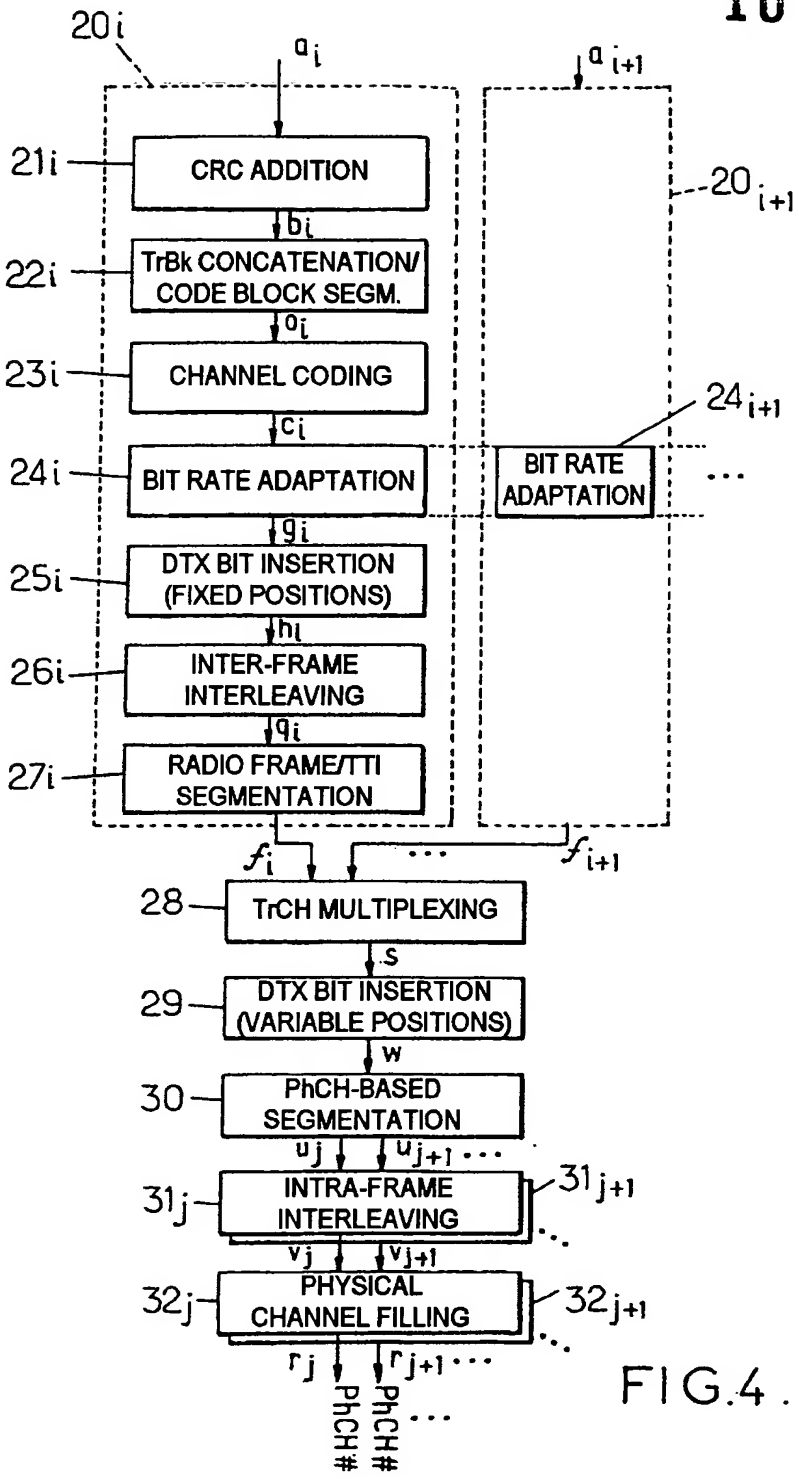

20i
ai
ai+1
21i
CRC ADDITION
bi
20i+1
22i
TrBk CONCATENATION/
CODE BLOCK SEGM.
oi
23i
CHANNEL CODING
ci
24i+1
24i
BIT RATE ADAPTATION
BIT RATE
ADAPTATION
gi
25i
DTX BIT INSERTION
(FIXED POSITIONS)
hi
26i
INTER-FRAME
INTERLEAVING
qi
27i
RADIO FRAME/TTI
SEGMENTATION
fi
fi+1
28
TrCH MULTIPLEXING
s
29
DTX BIT INSERTION
(VARIABLE POSITIONS)
w
30
PhCH-BASED
SEGMENTATION
uj
uj+1
31j
INTRA-FRAME
INTERLEAVING
31j+1
vj
vj+1
32j
PHYSICAL
CHANNEL FILLING
32j+1
rj
rj+1
PhCH#
PhCH#

FIG.4.

10/500900

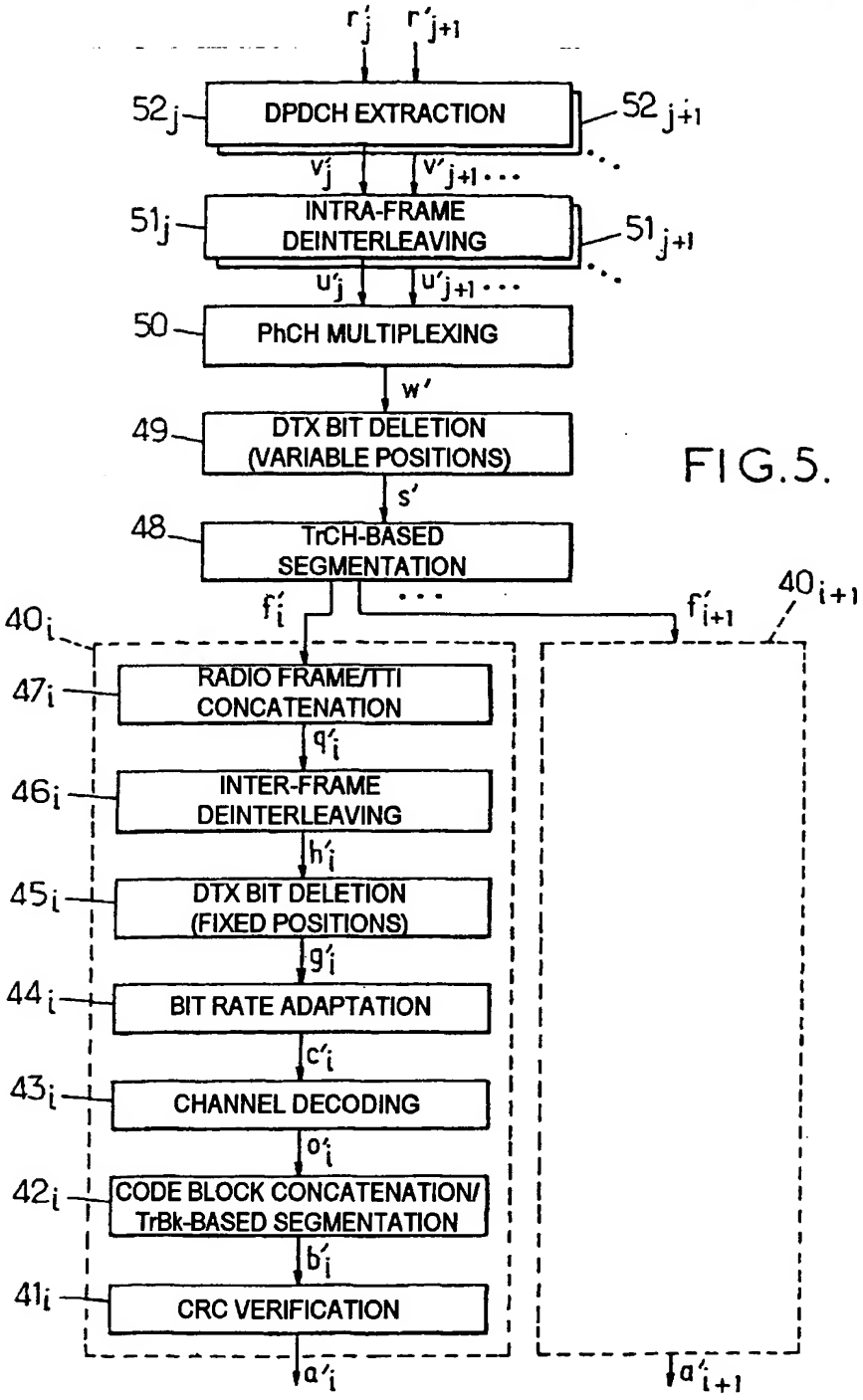


FIG.5.

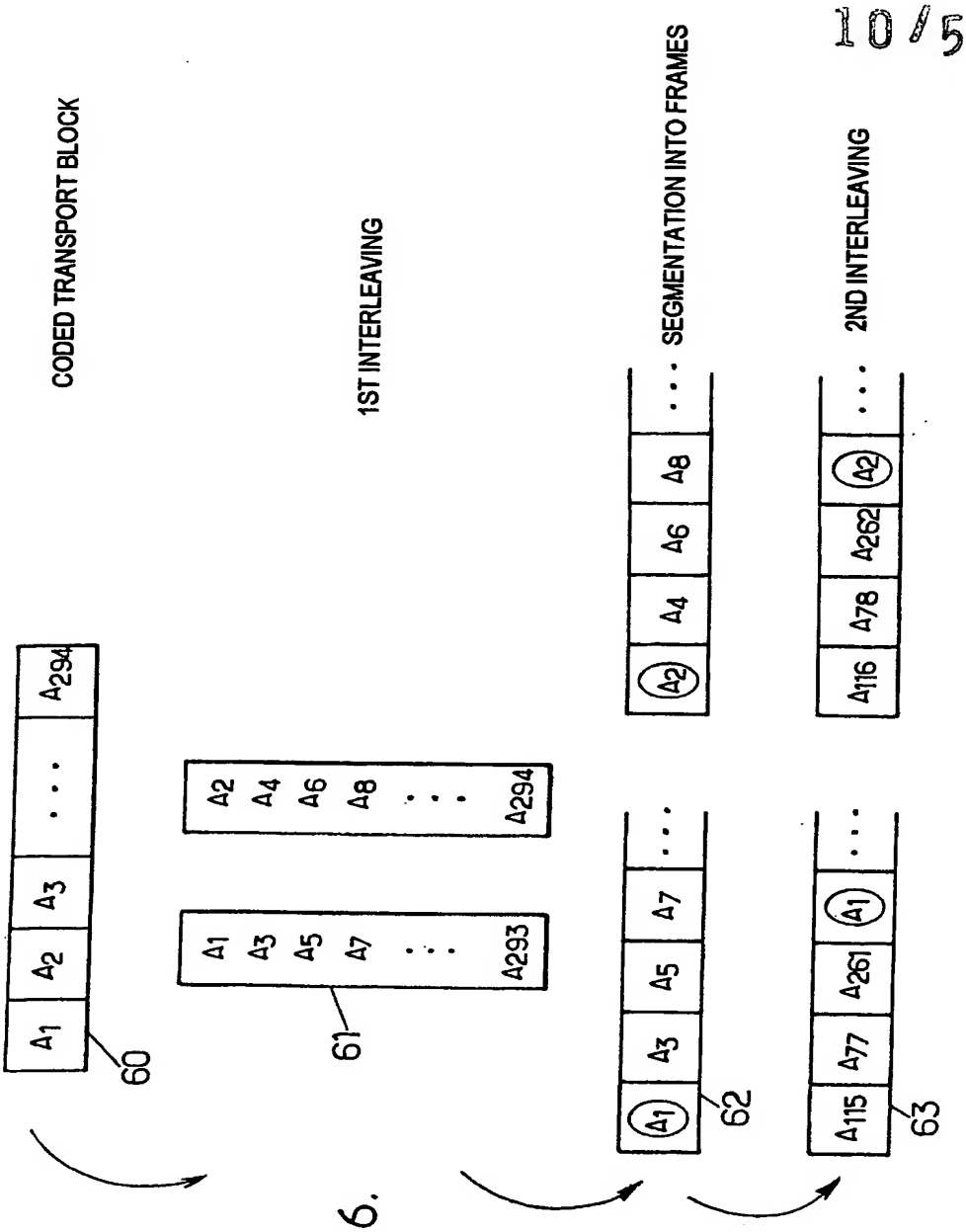

CODED TRANSPORT BLOCK
1ST INTERLEAVING
SEGMENTATION INTO FRAMES
2ND INTERLEAVING
A1 A2 A3 . . . A294
60
A1 A3 A5 A7 . . . A293
A2 A4 A6 A8 . . . A294
61
(A1) A3 A5 A7 . . .
(A2) A4 A6 A8 . . .
62
A115 A77 A261 (A1) . . .
A116 A78 A262 (A2) . . .
63

FIG.6.

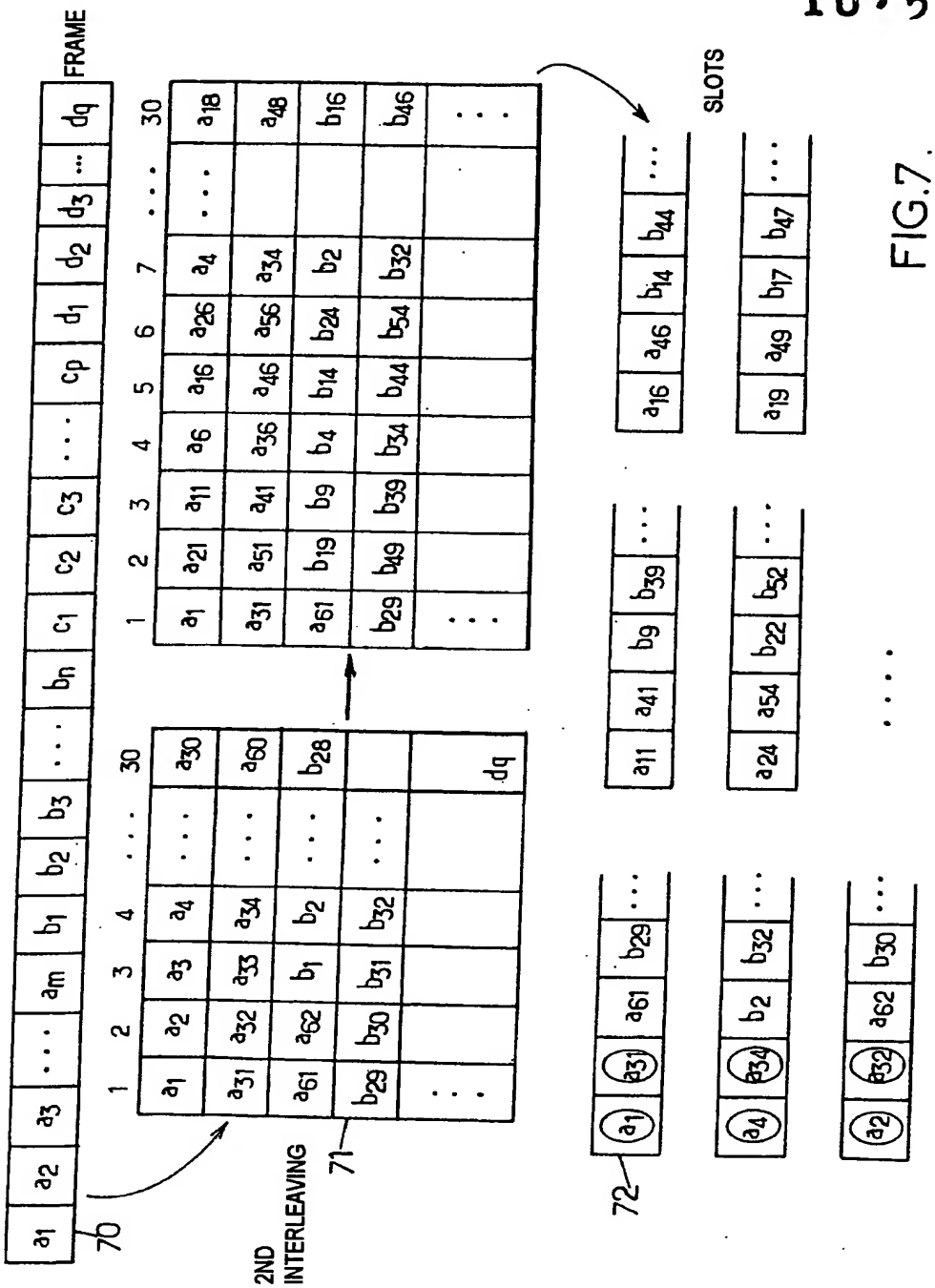


FIG.7.

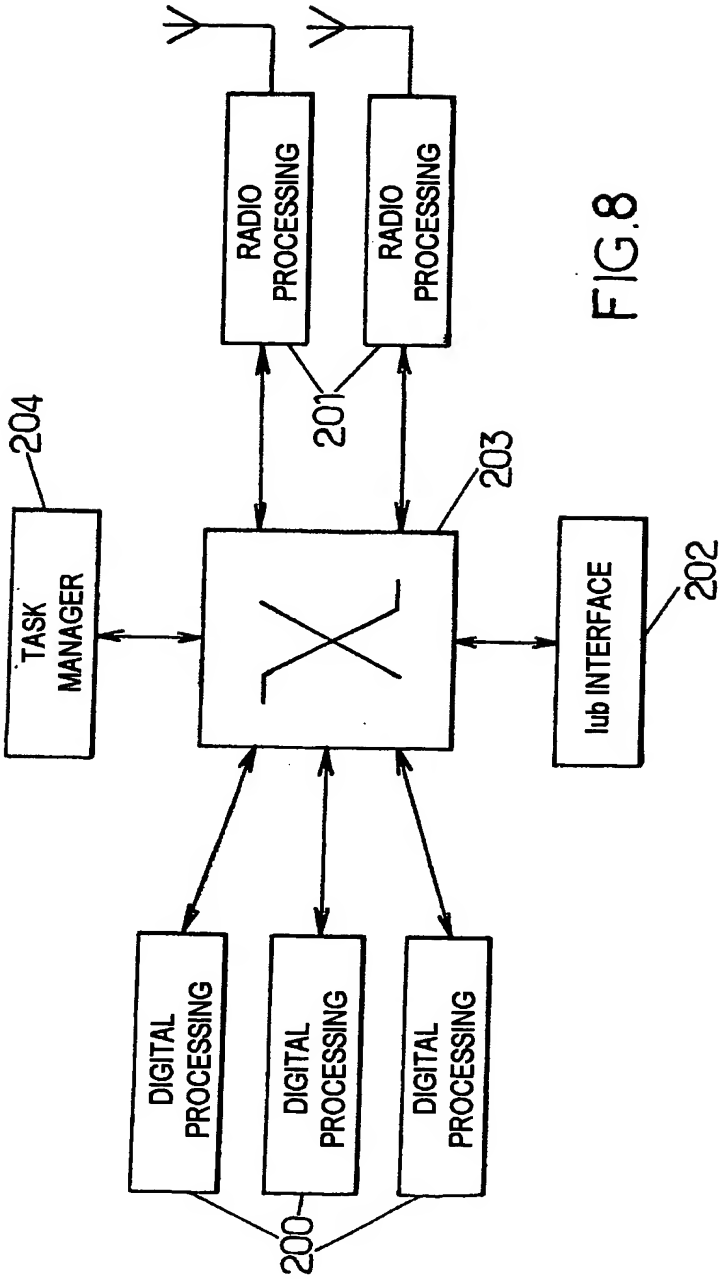

RADIO PROCESSING
RADIO PROCESSING
201
204
203
TASK MANAGER
Iub INTERFACE
202
DIGITAL PROCESSING
DIGITAL PROCESSING
DIGITAL PROCESSING
200

FIG.8

10/500900

SLOT No. / FRAME No.	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14
0	0/13			3			6			9			12		
1	3/1			6			9			12			0		
2	6/4			9			12			0			3		
3	9/7			12			0			3			6		

FIG.9.